

AMENDMENTS TO THE CLAIMS:

Kindly amend claims 1, 11 and 21, as shown below.

This listing of claims will replace all prior versions and listings of claims in the
Application:

Claim 1 (currently amended): A UV flame sensor comprising electronic circuitry
formed on a multi-layer Printed Circuit Board (PCB), said electronic circuitry including:

- a photodiode detecting an input signal;
- an amplifier amplifying said input signal and providing an output signal;
- a [[FET]] transistor providing automatic gain control; and
- at least one capacitor providing stability to the output signal of the amplifier;
- wherein said capacitor is formed from a capacitance laminate buried in said

PCB.

Claim 2 (original): The UV flame sensor of claim 1, wherein the sensor is adapted to
operate at temperatures of up to 125°C.

Claim 3 (original): The UV flame sensor of claim 1, further comprising at least one
guard band on each layer of the PCB.

Claim 4 (original): The UV flame sensor of claim 3, wherein said guard bands are
disposed at substantially identical positions in each said layer of the PCB.

Claim 5 (original): The UV flame sensor of claim 1, wherein the PCB has at least one
interior layer.

Claim 6 (original): The UV flame sensor of claim 5, further comprising tracks of equal potential disposed at substantially identical locations on each of the interior layers of the PCB.

Claim 7 (original): The UV flame sensor of claim 1, further comprising a low pass filter.

Claim 8 (original): The UV flame sensor of claim 3, wherein the guard bands on each said layer of the PCB are substantially identically shaped.

Claim 9 (original): The UV flame sensor of claim 6, wherein the tracks on each said layer of the PCB are substantially identically shaped.

Claim 10 (original): The UV flame sensor of claim 1, wherein said sensor has a response time of less than 25 milliseconds.

Claim 11 (currently amended): A UV flame sensor comprising:

a housing; and

~~lensing disposed within said housing; and~~

electronic circuitry formed on a multi-layer PCB and disposed within said housing;

wherein said circuitry includes:

a photodiode detecting an input signal through said lensing;

an amplifier amplifying said input signal and providing an output signal;

a [[FET]] transistor providing automatic gain control; and

at least one capacitor providing stability to the output signal of the amplifier;

and

wherein said capacitor is formed from a capacitance laminate buried in said PCB.

Claim 12 (original): The UV flame sensor of claim 11, wherein the sensor is adapted to operate at temperatures of up to 125°C

Claim 13 (original): The UV flame sensor of claim 11, further comprising at least one guard band on each layer of the PCB.

Claim 14 (original): The UV flame sensor of claim 13, wherein said guard bands are disposed at substantially identical positions in each said layer of the PCB.

Claim 15 (original): The UV flame sensor of claim 11, wherein the PCB has at least one interior layer.

Claim 16 (original): The UV flame sensor of claim 15, further comprising tracks of equal potential disposed at substantially identical locations on each of the interior layers of the PCB.

Claim 17 (original): The UV flame sensor of claim 13, wherein the guard bands on each said layer of the PCB are substantially identically shaped.

Claim 18 (original): The UV flame sensor of claim 16, wherein the tracks on each layer of the PCB are substantially identically shaped.

Claim 19 (original): The UV flame sensor of claim 11, further comprising a low pass filter.

Claim 20 (original): The UV flame sensor of claim 11, wherein the sensor has a response speed of less than 25 milliseconds.

Claim 21 (currently amended): A method for producing a UV flame sensor, said method comprising:

fabricating a PCB with at least three layers, at least one of said layers being an interior layer, and said layers having guard bands, ground planes, and tracks;

placing the guard bands in identical positions on each said layer of the PCB;

mimicking tracks of equal potential on the interior layers of the PCB;

burying a capacitance laminate on the interior layers of the PCB;

disposing on said PCB a photo diode for detecting an input signal, an amplifier for amplifying said input signal and providing an output signal, and a [[FET]] transistor providing automatic gain control; and

using the capacitance laminate buried on the interior layers of said PCB to form at least one capacitor so as to provide stability to the output signal of the amplifier.